

# GOVT VEER SURENDRA SAY PG COLLEGE, GARIABAND(C.G.)

## Second CIA Exam

Class: BCA-II Sem

Date: 23.05.2025

Subject: Digital Electronics (CASC-04T)

Max Mark: 20

*(Solve each question from section)*

### Section A: (Objective Types)

(Marks 4)

Q1. Which of the following is a type of digital logic circuit?

- A. Combinational Logic Circuit
- B. Sequential Logic Circuit
- C. Both
- D. None of Above

Q2. What is the Binary code of Hexadecimal code  $(1A5)_{16}$ ?

- A. 000110100101
- B. 000110100111
- C. 000110101101
- D. 000110111101

Q3. There are \_\_\_\_\_ cells in a 4-variable K-map?

- A. 8
- B. 32
- C. 12
- D. 16

Q4. Don't care conditions can be used for simplifying Boolean expressions in \_\_\_\_\_

- A. Registers
- B. Terms
- C. K-maps
- D. Latches

### Section B: (Short Descriptive Answer)

(Marks 6)

Q5. How many types of Boolean law? Write only name of law?

Q6. What is Flip flop? Explain its types with truth table?

Q7. What is Number System? Explain its types?

### Section C: Solve Any 2 (Long Descriptive Answer)

(Marks 10)

Q8. Simplify  $F(ABCD) = \sum (1,2,3,6,8,9,10,12,13)$  using K-Map?

Q9. Write the brief note (block diagram) about Microprocessor?

Q10. What is Error correcting and detecting code? describe its types with suitable example?